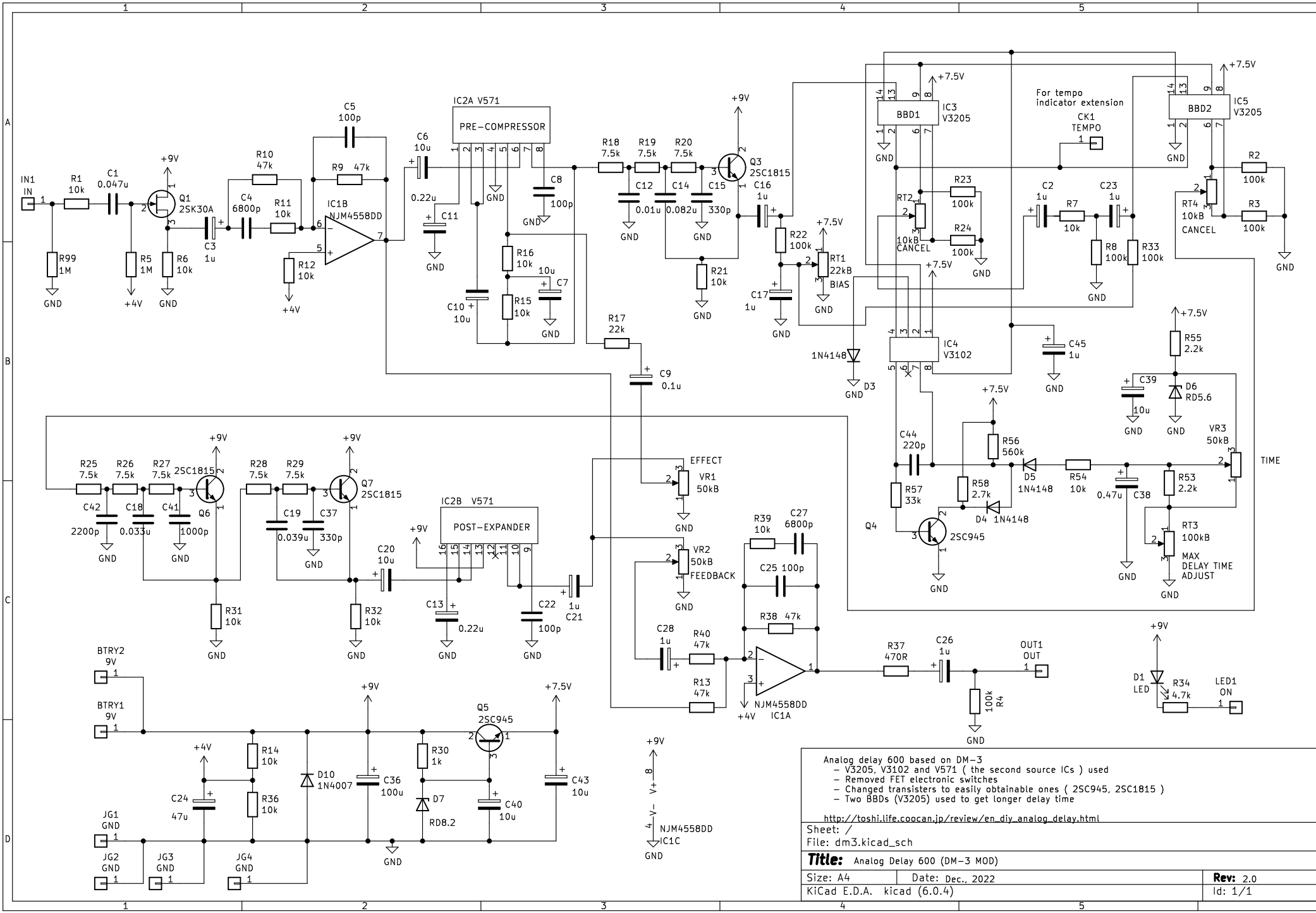
IN1
IN
R1
10k
C1
0.047u
Q1
2SK30A
+9V
R99
1M
R5
1M
R6
10k
C3
1u
C4
6800p
R10
47k
R11
10k
R12
10k
+4V
GND
C5
100p
R9 47k
IC1B
NJM4558DD
C6
10u
0.22u
C11
IC2A V571
PRE-COMPRESSOR
C8
100p
C10
10u
R16
10k
R15
10k
C7
10u
R18
7.5k
R19
7.5k
R20
7.5k
C12
0.01u
C14
0.082u
C15
330p
R21
10k
Q3
2SC1815
C16
1u
R22
100k
C17
1u
R17
22k
C9
0.1u
RT1
22kB
BIAS
+7.5V
1N4148
D3
BBD1
IC3
V3205
RT2
10kB
CANCEL
R23
100k
R24
100k
For tempo
indicator extension
CK1
TEMPO
BBD2
IC5
V3205
C2
1u
R7
10k
C23
1u
R8
100k
R33
100k
RT4
10kB
CANCEL
R2
100k
R3
100k
IC4
V3102
C45
1u
R55
2.2k
C39
10u
D6
RD5.6
VR3
50kB
TIME
C44
220p
R56
560k
D5
1N4148
R54
10k
0.47u
C38
R53
2.2k
RT3
100kB
MAX
DELAY TIME
ADJUST
R57
33k
R58
2.7k
D4 1N4148
Q4
2SC945
R25
7.5k
R26
7.5k
R27
7.5k
2SC1815
Q6
C42
2200p
C18
0.033u
C41
1000p
R31
10k
R28
7.5k
R29
7.5k
Q7
2SC1815
C19
0.039u
C37
330p
R32
10k
IC2B V571
POST-EXPANDER
C20
10u
C13
0.22u
C22
100p
C21
1u
EFFECT
VR1
50kB
VR2
50kB
FEEDBACK
R39
10k
C27
6800p
C25 100p
R38 47k
C28
1u
R40
47k
R13
47k
NJM4558DD
IC1A
R37
470R
C26
1u
OUT1
OUT
100k
R4
D1
LED
R34
4.7k
LED1
ON
BTRY2
9V
BTRY1
9V
Q5
2SC945
R14
10k
R36
10k
C24
47u
D10
1N4007
C36
100u
R30
1k
D7
RD8.2
C40
10u
C43
10u
JG1
GND
JG2
GND
JG3
GND
JG4
GND
V+
V-
NJM4558DD
IC1C
Analog delay 600 based on DM-3
- V3205, V3102 and V571 (the second source ICs) used
- Removed FET electronic switches
- Changed transisters to easily obtainable ones (2SC945, 2SC1815)
- Two BBDs (V3205) used to get longer delay time
http://toshi.life.coocan.jp/review/en_diy_analog_delay.html
Sheet: /
File: dm3.kicad_sch
Title: Analog Delay 600 (DM-3 MOD)
Size: A4
Date: Dec., 2022
Rev: 2.0
KiCad E.D.A. kicad (6.0.4)
Id: 1/1